

## Interview Summary

Application No. 09/748,589

Applica

March et al.

Examiner

Gary J. Portka

Art Unit 2187



All participants (applicant, applicant's representative, PTO personnel):	
(1) Gary J. Portka (PTO)	(3) Thomas Lee (Applicant)
(2) Joseph Hetz (Applicant's Representative)	(4) Liza Toth (Applicant's Representative)
Date of Interview Jul 25, 2002	_
Type: a) ☒ Telephonic b) ☐ Video Conference c) ☐ Personal [copy is given to 1) ☐ applicant  Exhibit shown or demonstration conducted: d) ☐ Yes	
Claim(s) discussed: 114	
Identification of prior art discussed:  Leedy, U.S. Patent 6,208,545 B1.	
any other comments:  The Applicant and Applicant's representatives argued that vertical stack of layers as described by Leedy; they propose the term. Examiner agreed to review this argument and examiner agreed to review this argument.	I nature of what was agreed to if an agreement was reached, or  the claim term "chip" signified a monolithic circuit and not a sed sending references that show the recognized meaning of vidence.
allowable, if available, must be attached. Also, where no available, a summary thereof must be attached.)	dments which the examiner agreed would render the claims copy of the amendments that would render the claims allowable is
Unless the paragraph above has been checked, THE FORM INCLUDE THE SUBSTANCE OF THE INTERVIEW. (See MP already been filed, APPLICANT IS GIVEN ONE MONTH FRO	Trate record of the substance of the interview (if box is checked).  IAL WRITTEN REPLY TO THE LAST OFFICE ACTION MUST PEP section 713.04). If a reply to the last Office action has OM THIS INTERVIEW DATE TO FILE A STATEMENT OF THE rd of Interview requirements on reverse side or on attached
Examiner Note: You must sign this form unless it is an Attachment to a signed Office action.	Examiner's signature, if required